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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Phadnis et al

Appl. No.: 09/976,004

Filed: October 15, 2001

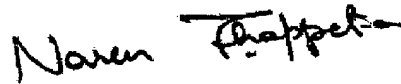
For: Reducing Overhead When Setting Up Multiple
Virtual Circuits Using Signaling Protocols

Art Unit: 2661

Examiner: Robert W. Wilson

Atty. Docket: CSCO-010/4390

I hereby certify that this correspondence is being facsimile
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Office (USPTO) (Fax No. 571-273-8300) on April 13,
2006



(Signature)

By: Narendra R. Thappeta (Type Name)

TRANSMITTAL LETTER

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In connection with the above-referenced U. S. patent application, transmitted herewith are
the following papers:

- [x] This Cover Letter (2 Pages in duplicate);
- [x] Petition for extension of time under 37 CFR § 1.136(a) (1 Page);
- [x] Credit Card Authorization Payment Form for \$120.00 to cover the one-month extension of
time under 37 CFR § 1.136(a) (1 Page); and
- [x] Amendment and Response Under 37 C.F.R. §§ 1.111 (18 Pages).

Page 1 of 2

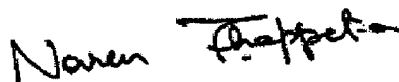
April 13, 2006

Attorney Docket No.: CSCO-010/4390

Appl. No.: 09/976,004

The Commissioner is hereby authorized to charge underpayment of any additional fees or credit any overpayment associated with this communication or any future or past communication associated with the subject matter to Deposit Account No.: 20-0674. If any extensions of time are required to prevent abandonment of the present application, then such extensions of time are hereby petitioned, and any fees therefor are hereby authorized to be charged to Deposit Account No.: 20-0674. A duplicate copy of this authorization is enclosed

Respectfully submitted,



Date: April 13, 2006

By: _____
Narendra Reddy Thappeta
Registration No. 41,416

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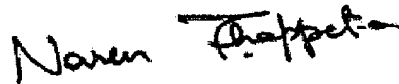
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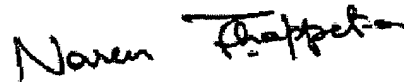
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